

WHAT IS CLAIMED IS:

1. A display panel for image display, said display panel comprising a plurality of data lines for transferring a data voltage representing an image signal, a plurality of scan lines for transferring a selection signal, and a plurality of pixel circuits , each pixel circuit being coupled to a corresponding said data line and two adjacent said scan lines, each pixel circuit comprising:

a display element capable of displaying a portion of an image, the image portion corresponding to a quantity of applied current;

a first transistor having a main electrode and a control electrode;

a capacitor coupled between the main electrode and the control electrode of the first transistor, wherein the first transistor is capable of generating the applied current in response to voltage between the main electrode and the control electrode;

a second transistor having a control electrode coupled to the control electrode of the first transistor, the second transistor being configured to operate as a diode;

a first switching element coupled to a main electrode of the second transistor, wherein the first switching element transfers the data voltage from the data lines to the second transistor in response to the selection signal from one of the two adjacent scan lines;

a second switching element for transferring a precharge voltage to the control electrode of the first transistor in response to a first control signal before the data voltage is supplied; and

a third switching element being turned off in response to a second

control signal for electrically isolating the first transistor from the display element.

2. The display panel as claimed in claim 1, wherein the third switching
5 element is coupled between the first transistor and the display element.

3. The display panel as claimed in claim 1, wherein the two adjacent scan lines comprise a current scan line and a previous scan line, and said one of the two adjacent scan lines is the current scan line.

10 4. The display panel as claimed in claim 3, wherein the first control signal is the selection signal from the previous scan line.

5. The display panel as claimed in claim 4, wherein the data voltage is
15 applied to the data lines after transferring the precharge voltage in response to the first control signal and before applying the selection signal to the current scan line.

6. The display panel as claimed in claim 5, wherein the data voltage in
20 the data lines is changed to a desired voltage before the select signal is applied to the current scan line.

7. The display panel as claimed in claim 3, wherein the second control signal includes the first control signal.

8. The display panel as claimed in claim 7, wherein the selection signal from the previous scan line is used as both the first and second control signals, and

5 the second switching element comprises a transistor of a first conductive type, the third switching element comprises a transistor of a second conductive type, the second conductive type being an opposite of the first conductive type.

10 9. The display panel as claimed in claim 3, wherein the selection signal from the current scan line is used as the second control signal, and

 the second switching element comprises a transistor of a first conductive type, the third switching element comprises a transistor of a second conductive type, the second conductive type being an opposite of the first
15 conductive type.

 10. The display panel as claimed in claim 9, wherein the selection signal from the previous scan line is used as the first control signal.

20 11. The display panel as claimed in claim 3, wherein the third switching element is turned off during a time period of transferring the precharge voltage using the first control signal and another time period of transferring the data voltage using the selection signal from the current scan line.

12. The display panel as claimed in claim 11, wherein the third switching element comprises third and fourth transistors coupled in series,

the second control signal comprising a third control signal for turning the third transistor off during the time period of transferring the precharge voltage, and a fourth control signal for turning the fourth transistor off during
5 said another time period of transferring the data voltage.

13. The display panel as claimed in claim 12, wherein the selection signal from the previous scan line are used as both the first and third control
10 signals,

the second switching element is a transistor of a first conductive type, the third switching element is a transistor of a second conductive type, and the second conductive type is an opposite of the first conductive type.

14. The display panel as claimed in claim 12, wherein the fourth control
15 signal is a selection signal from the current scan line, and

the fourth transistor is a transistor of a type that is opposite of the type of the first transistor.

15. The display panel as claimed in claim 1, wherein the first and
20 second switching elements are transistors of the same type as the first and second transistors.

16. The display panel as claimed in claim 1, wherein the precharge

voltage is lower than a lowest data voltage from the data lines.

17. An image display device comprising:

the display panel according to claim 1;

5 a data driver mounted on the display panel or coupled to the display panel, said data driver being capable of applying the data voltage to the data lines; and

a scan driver mounted on the display panel or coupled to the display panel, said scan driver being capable of applying the selection signal to the scan lines.

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18. A method for driving an image display device coupled to two adjacent scan lines the image display device comprising a first transistor having a main electrode and a control electrode; a capacitor coupled between the main electrode and the control electrode of the first transistor, the first transistor being capable of generating a current corresponding to a voltage charged in the capacitor, a second transistor having a control electrode coupled to the control electrode of the first transistor and being configured to operate as a diode, and a display element capable of displaying a portion of an image corresponding to a quantity of the current generated by the first transistor, the method comprising:

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transferring a precharge voltage to the control electrode of the first transistor in response to a first control signal during a first time period;

transferring a data voltage to the control electrode of the first transistor

through the second transistor in response to a selection signal from one of the two adjacent scan lines during a second time period; and

interrupting the transfer of the data voltage,

wherein the first transistor is electrically isolated from the display element during at least one of the first time period and the second time period.

19. The method as claimed in claim 18, wherein the first transistor is electrically isolated from the display element in response to the first control signal during the first time period.

20. The method as claimed in claim 18, wherein the two adjacent scan lines comprise a current scan line and a previous scan line, wherein said one of the two adjacent scan lines is the current scan line.

21. The method as claimed in claim 20, wherein the first control signal is a selection signal from the previous scan line.

22. The method as claimed in claim 19, wherein the first transistor is electrically isolated from the display element in response to the selection signal from said one of the two adjacent scan lines during the second time period.

23. The method as claimed in claim 20, wherein the first transistor is electrically isolated from the display element in response to a second control signal during the second time period.

24. The method as claimed in claim 23, wherein the second control signal is the selection signal from the current scan line.

5 25. The method as claimed in claim 20, further comprising:
preventing the precharge voltage and the data voltage from being transferred to the control electrode of the first transistor between the first and second time periods.

10 26. The method as claimed in claim 25, wherein the first control signal is a selection signal from the previous scan line,

the first transistor is electrically isolated from the display element in response to the selection signal from the previous scan line during the first time period, and

15 the first transistor is electrically isolated from the display element in response to the selection signal from the current scan line during the second time period.

20 27. A pixel circuit, which responds to a precharge voltage from a first signal line and a data voltage representing an image signal from a second signal line, the pixel circuit comprising:

a first transistor having a main electrode and a control electrode;

a capacitor coupled between the main electrode and the control electrode, wherein the first transistor is capable of generating a current in

response to a voltage charged in the capacitor;

a second transistor having a control electrode coupled to the control electrode of the first transistor, the second transistor being configured to operate as a diode;

5 a display element capable of displaying a portion of an image, said image portion corresponding to the current generated by the first transistor; and

switching means coupled between the first transistor and the display element,

wherein the precharge voltage is applied to the control electrode of the first transistor in response to a control signal for a first time period, and the data voltage is applied to the control electrode of the first transistor in response to a select signal for a second time period, and the first transistor is electrically isolated from the display element by the switching means during at least one of the first time period and the second time period.

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28. The pixel circuit as claimed in claim 27, wherein the control signal is a previous select signal.

29. A display device comprising:

20 a display element for displaying a portion of an image in response to a current being applied;

a first transistor having a main electrode and a control electrode, and coupled between a voltage source and the display element;

a capacitor coupled between the main electrode and the control

electrode, wherein the first transistor is capable of generating the current in response to a charge in the capacitor; and

a first switching element coupled between the first transistor and the display element to interrupt the current to the display element while charging the capacitor using at least one of a precharge voltage and a data voltage representative of the image portion.

30. The display device of claim 29, further comprising a second switching element coupled to a first selection signal, wherein, when the first selection signal is activated, the second switching element allows the precharge voltage to be applied to the capacitor for charging and the first switching element is turned off to prevent the current from flowing to the display element.

31. The display device of claim 29, further comprising a third switching element coupled to a second selection signal, wherein, when the second selection signal is activated, the third switching element allows the data voltage to be applied to the capacitor for charging and the first switching element is turned off to prevent the current from flowing to the display element.

32. The display device of claim 30, further comprising a third switching element coupled to a second selection signal, wherein, when the second selection signal is activated, the third switching element allows the data voltage to be applied to the capacitor for charging and the first switching element is turned off to prevent the current from flowing to the display element.

33. The display device of claim 32, wherein there is a time period between when the first selection signal is un-activated and when the second selection signal is activated.

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34. The display device of claim 31, wherein the first switching element is turned on to allow the current to flow to the display element when the second selection signal is un-activated after the capacitor has been charged using the data voltage.

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35. The display device of claim 29, further comprising a second transistor having a control electrode coupled to the control electrode of the first transistor, wherein the second transistor is configured to operate as a diode.

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36. The display device of claim 30, further comprising a second transistor having a control electrode coupled to the control electrode of the first transistor, said control electrodes being coupled to the precharge voltage via the second switching element, wherein the second transistor is configured to operate as a diode.

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37. The display device of claim 31, further comprising a second transistor having a control electrode and a main electrode, wherein the control electrode of the second transistor is coupled to the control electrode of the first transistor, the main electrode of the second transistor is coupled to the data

voltage via the third switching element, and the second transistor is configured to operate as a diode.